

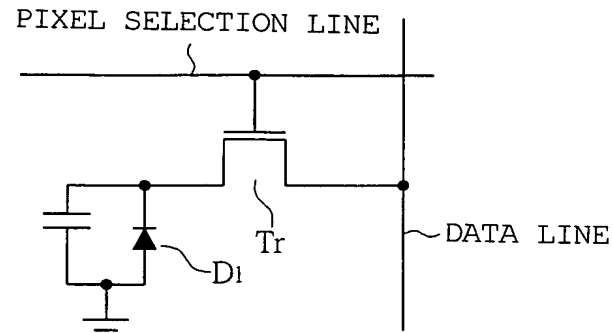
[illegible]

FIG. 2

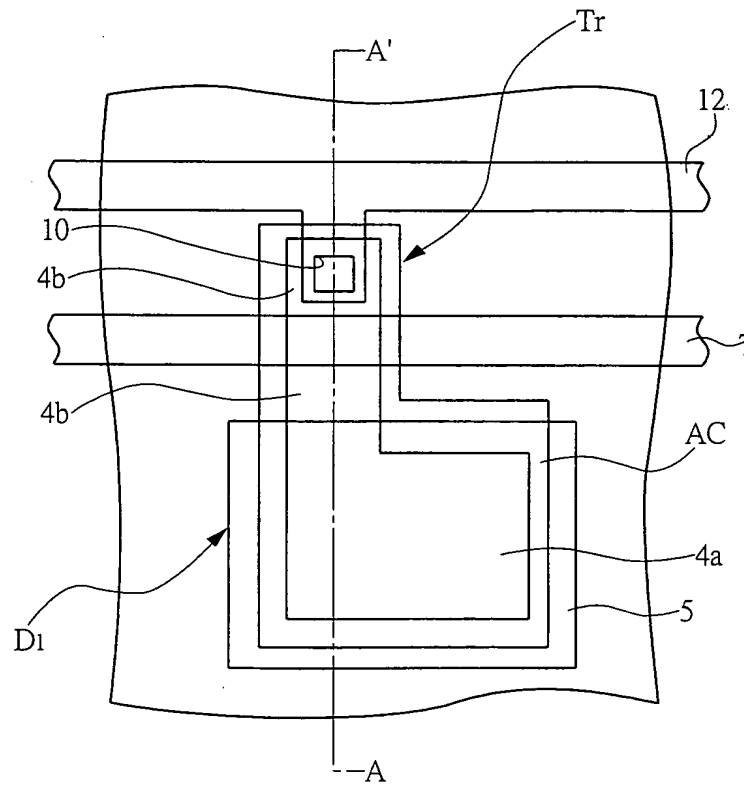


Figure 1 is a semi-logarithmic plot showing the relationship between LEAK CURRENT (A) on the y-axis and VOLTAGE (V) on the x-axis. The y-axis is logarithmic, ranging from $1.E-15$ to $1.E-3$. The x-axis is linear, ranging from 0 to 7 V. Two curves are plotted: a solid line labeled D_1 and a dashed line labeled D_0 . Both curves show a sharp increase in current starting around 5.5 V. The D_1 curve rises more steeply than the D_0 curve.

FIG. 5

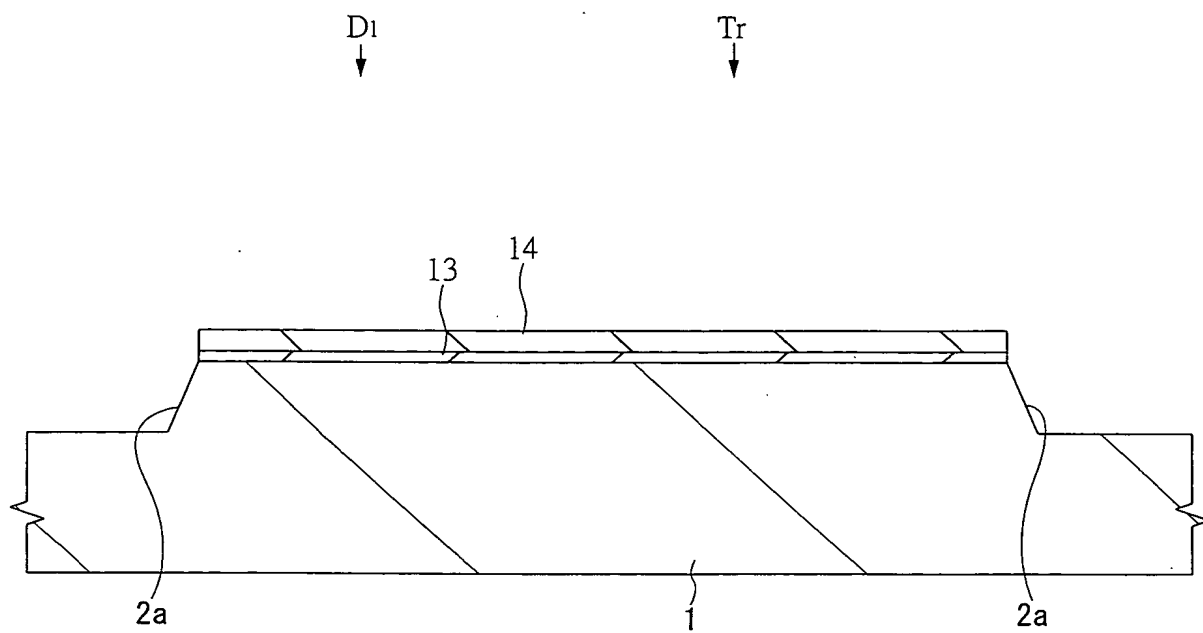


FIG. 6

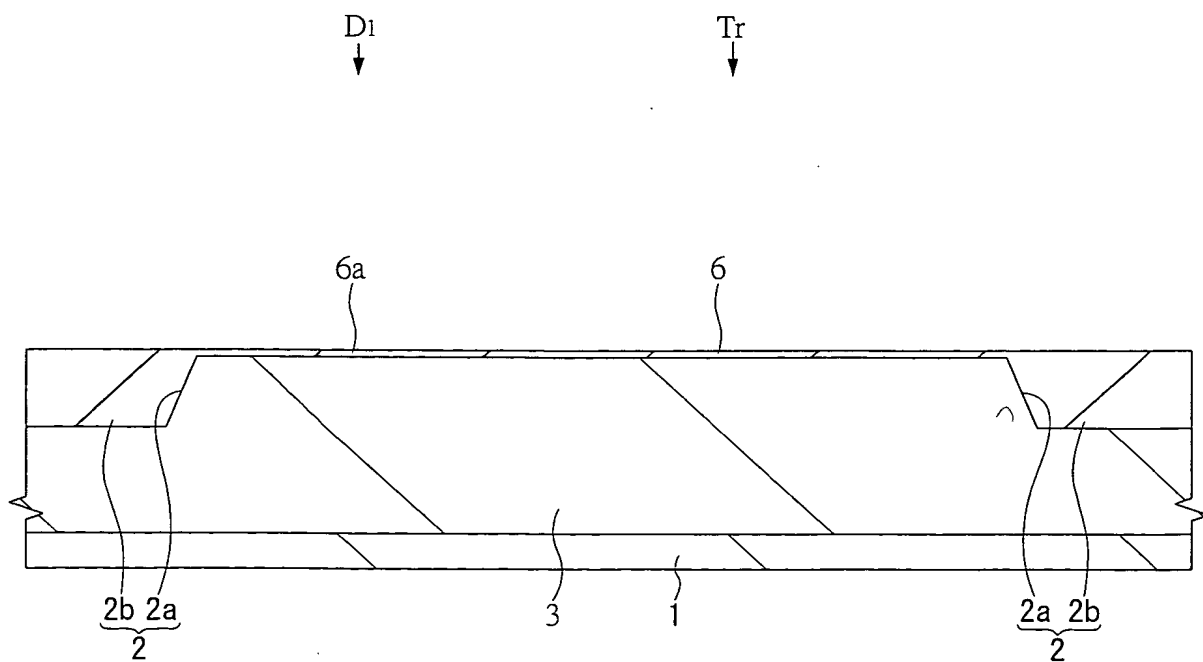


FIG. 7

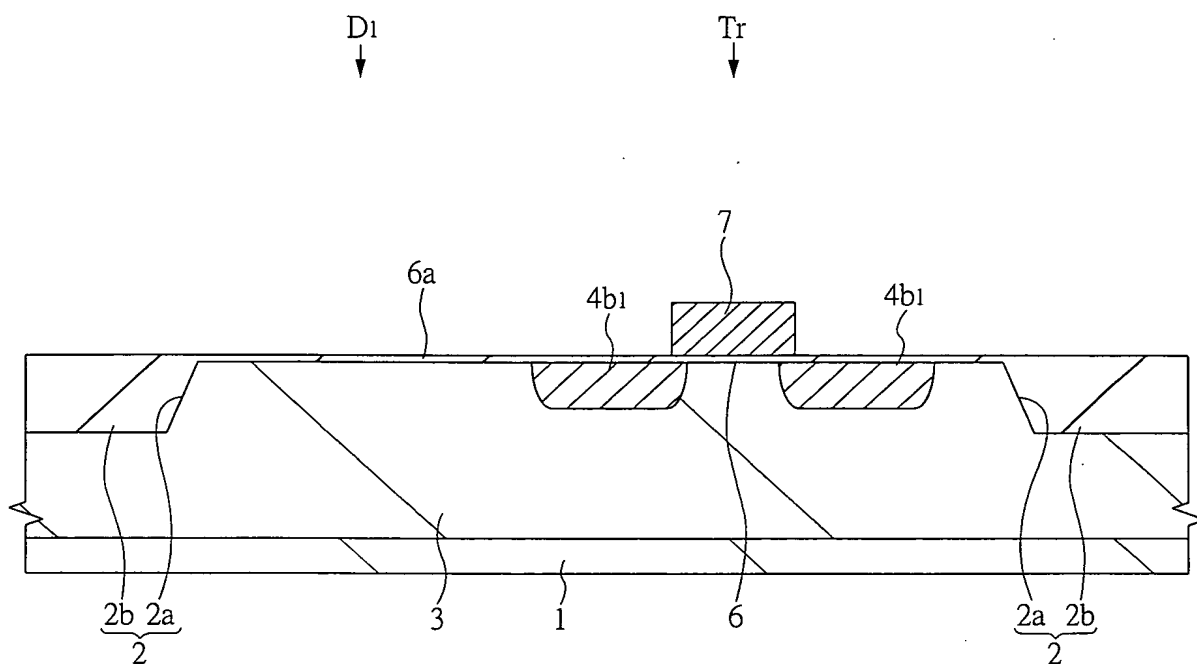
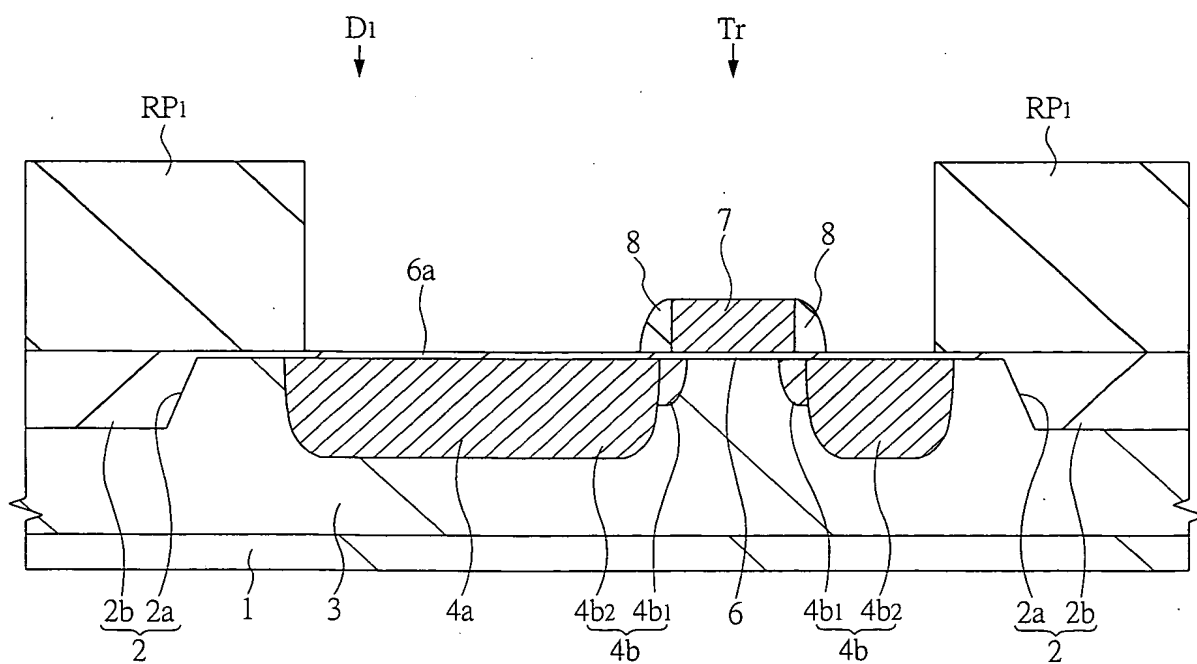


FIG. 8



[illegible][illegible]

FIG. 11

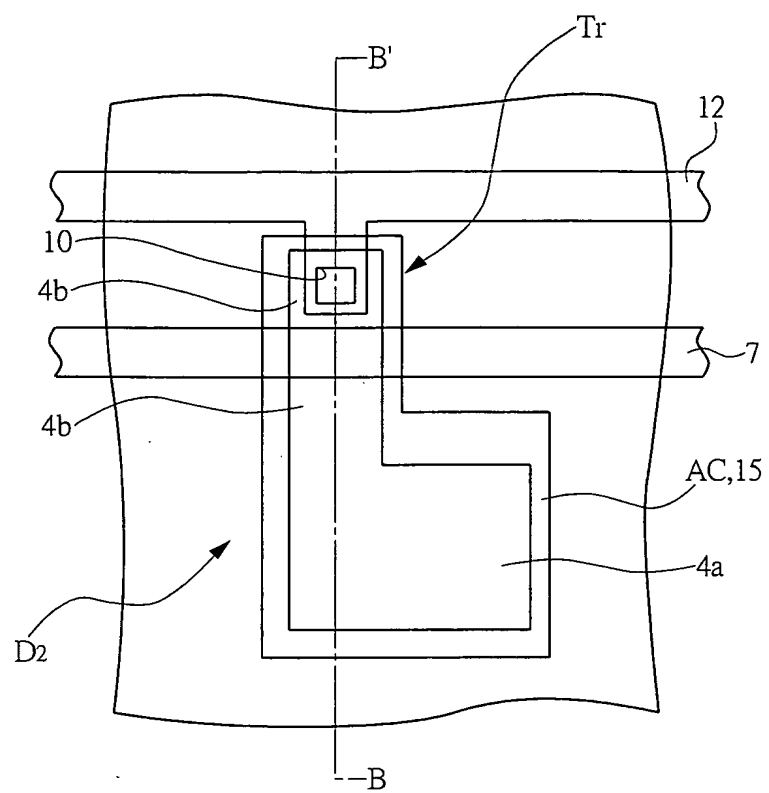


FIG. 12

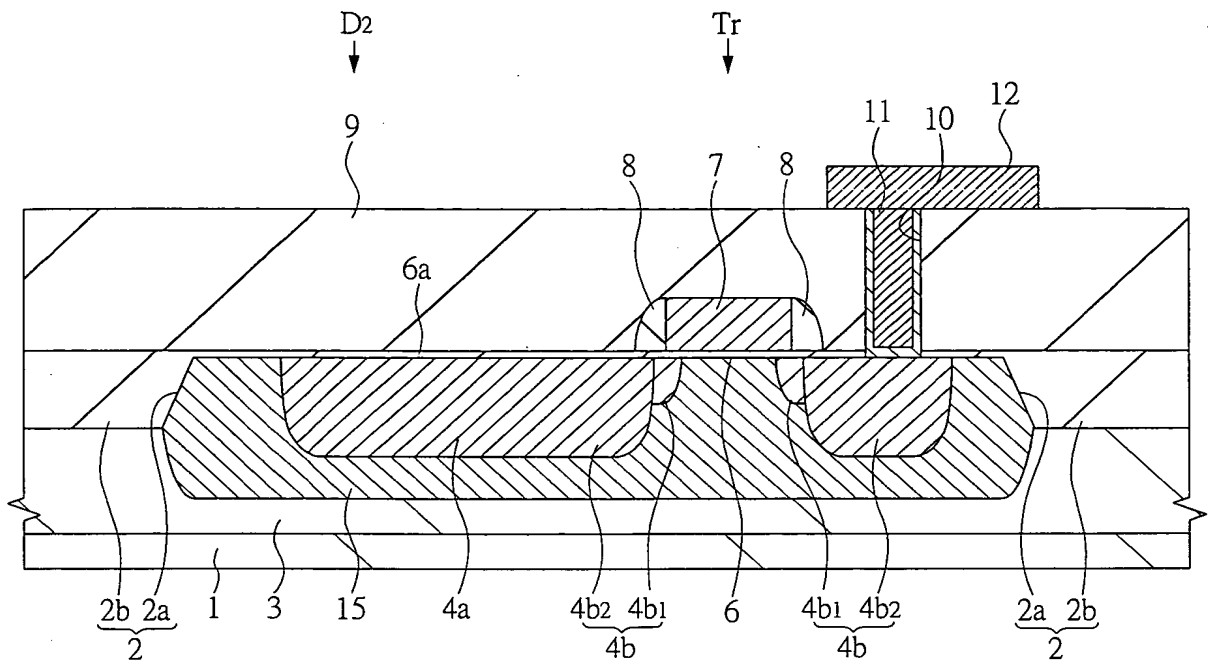


Diagram illustrating a cross-sectional view of a semiconductor device. The device includes a substrate 1 with a top layer 2. A central region 3 contains a structure 4a, 4b1, 4b2, 6, 7, 8, 9, 10, 11, 12. A bottom layer 16 is shown. Arrows D3 and Tr indicate forces or directions.

[illegible]